In the Specification

Please replace the paragraph beginning on page 8, line 1 and ending on page 8, line 10 with the following amended paragraph.

Figure 1 is a diagrammatic representation showing implementation of an electronic device using various design automation tools that can be tested using the mechanisms of the present invention. An input stage 101 receives selection information typically from a user for logic such as a processor as well as other components to be implemented on an electronic device. A generator program 105 creates a logic description 103 and provides the logic description 103 along with other customized logic to any of a variety of synthesis tools, place and route programs, and logic configuration tools to allow a logic description to be implemented on an electronic device.

Please replace the paragraph beginning on page 12, line 7 and ending on page 12, line 17 with the following amended paragraph.

Although testing of a design automation tool 311 can be conducted in this manner, there are possible alternative mechanisms for testing the design automation [[toll]] tool. In one example, randomized inputs and randomized logic can be provided to a design automation tool 311. However, randomized logic or randomized inputs may not accurately reflect real world designs and may still be provided to a tool. Resources would then have to be devoted to developing or correcting a design automation tool to handle even these nonreal world situations. Consequently, techniques of the present invention provide mechanisms for providing design automation tools with a large and diverse set of real world test designs. The test designs can be randomly generated yet include sufficient operability to test various mechanisms supported by the design automation tool 311.